Sensorless Voltage Observer for a Current-Fed High Step-Up DC-DC Converter Using Extended Kalman Filter

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Abstract: In high voltage applications, sensorless voltage control techniques can reduce the cost and increase the reliability of DC-DC converters. In this paper, a sensorless voltage observer for a current fed Cockcroft-Walton voltage multiplier is designed. The first step is to derive the converter model. Since any inaccuracy in the derived model can result in a discrepancy between the observed voltage and the actual output voltage, an accurate model is derived, which incorporates the influential elements. Then, two voltage observers based on the extended Kalman filter (EKF) are designed and used to estimate the output voltage, transformer magnetizing current and inductor current for two different configurations of the high step-up DC-DC converter. Experimental and simulation results of the system show the efficiency of the observers. The proposed observers represent good precision as the main parasitic parameters are considered in the converter model.

Keywords: high step-up DC-DC converter; sensorless voltage observer; extended Kalman filter

1. Introduction

During the past few decades, high-voltage DC power supplies have been widely used in industry, medicine, science, military, X-ray systems, dust filtering, insulating tests, and electrostatic coating. Today, most industrial high voltage loads are fed by line-commutated thyristor-based high voltage (HV) power supplies which are not able to provide optimal HV waveforms and are not cost-efficient in their processes [1]. The emerging topology for generating high DC voltage is shown in Figure 1. The interface system is a two-stage structure. The first stage is a diode rectifier which provides low DC voltage, then in the second stage, a switching-mode DC-DC converter boosts the DC voltage [2–5]. The wide applications of these switching-mode power supplies necessitate the design of precise, low cost and highly efficient DC-DC converters. However conventional voltage control strategies need to sample the output voltage using an accurate voltage sensor. Employing isolated high voltage DC-voltage sensors for isolated structures imposes more complexity, reduces reliability and adds extra hardware cost to the system. Thus, sensorless voltage control techniques, which are cost-effective and accurate enough, can be used. In order to apply sensorless voltage control, a voltage observer is used to estimate the output. It is very important to derive an accurate model of the converter since the precision of the system modelling affects the performance of the voltage observer and any inaccuracy in the derived model can deviate the observed voltage from the actual output voltage.
The extended Kalman filter (EKF) is used for observation of the output voltage. Since the 1960s, Kalman filter (KF) theory has been employed in many fields such as electrical engineering and robotics [6]. In electrical power engineering, KF and EKF have been widely used for observation purposes and parameter estimation in electric machines control and power electronic converters. KF and EKF are used for flux or speed estimation of induction motors in different control strategies such as direct torque control [7–10]. The rotor flux of the induction motor is estimated by using the stator voltages and currents and rotor speed measurement [11]. EKF has also received increasing attention in the estimation of different parameters of permanent magnet synchronous motors (PMSMs). For instance, in a PMSM, rotor speed and position has been estimated by using an EKF [12]. In order to decrease the computation resources, a reduced-order observer-based on EKF has been proposed which also enhances the estimation accuracy of the speed and rotor flux angle of a PMSM [13]. An EKF has been employed to estimate the speed of a PMSM in conjunction with an optimal method to adjust the matrices of EKF [14]. In order to attain sensorless speed control of a DC motor, the results of the speed estimation by using artificial neural network (ANN) and EKF have been compared in [15]. However, only simulation results have been proposed. The analysis of model-structure uncertainties effects on the robustness and performance of the Takagi-Sugeno observer and different types of Kalman filter for estimation of effective wind speed have been presented in [16]. Remaining useful life estimation of bearings under different operating conditions has been performed using an EKF [17]. In order to implement model predictive control for a boost converter in [18], the capacitor voltage and the inductor current of the converter have been estimated through an EKF. A Kalman filter has been used to consider variations of the load and minimize the steady-state error of the output voltage in control of the boost and synchronous step-down DC-DC converters [19–21]. The disturbances in the linearized model of a three phase inverter have been estimated in [22] by using a new derivative-free nonlinear Kalman Filter. In [23], the load current of a three-cell converter has been measured and the capacitor voltages have been estimated using a Kalman filter. Consequently, the number of voltage sensors has been reduced. The Capacitor voltages of modular multilevel converters have been estimated through an adaptive Kalman filter based observer in [24]. This method has also reduced the number of voltage sensors. Sliding mode control of a Single Ended Primary Inductor Converter (SEPIC) has been performed by applying an EKF for estimation of the state variables and variation of the load [25]. A sensorless predictive current control (PCC) strategy has been implemented by using EKF for a boost converter which is a second-order system [26]. Generally, other types of Kalman filter which can be applied to estimate converter voltage are as follows:

- **Kalman filter (KF):** KF is applied to the linear systems, therefore its accuracy is limited to the operating point at which the converter can be modelled as a linear system.
- **Unscented Kalman filter (UKF):** UKF is suitable for a highly nonlinear system. UKF uses unscented transformation (UT) to pick a minimal set of sample points around the mean value. It is used only in the cases that the extended Kalman filter can give particularly poor performance. In the paper, it is shown that EKF has an appropriate performance to estimate the converter voltage.
In some other researches, different types of KF are used for capacitor voltage estimation. In [27,28], KF is used for voltage estimation and applied in the voltage-balancing control of the multi-level converters and its performance is evaluated in [29]. In [27], the adaptive linear neuron (ADALINE) algorithm is employed for estimating of capacitor voltage in multi-level converters. One of the main constraints of using EKF for voltage estimation and control purposes is related to its computational burden. In [30], a method of computational complexity reduction in EKFs is proposed that is designed for systems with non-linear measurement models.

Recently, a current-fed high step-up DC-DC converter based on the Cockcroft-Walton Voltage Multiplier (CWVM) has been introduced to provide high DC voltage with several advantages as follows [31,32]:

- Low and uniform stress per stage on diodes and capacitors.
- Wide range of multiplication stages is achieved.
- Compactness, low weight, and cost-efficiency.
- Negative output through reversing diode polarity.
- Low input current ripple.
- Providing a part of the voltage gain by the current-fed circuit.

The output voltage of a current-fed high step-up CWVM DC-DC converter, which is a fourth-order system and more complicated compared to the investigated converters in the literature, is estimated in this paper. Moreover, since the output voltage of the converter is usually high, employing an isolated high voltage sensor can be very costly while reducing the system reliability. Hence, the voltage observer appears to be a viable alternative to the voltage sensor. In order to implement the voltage observer, an accurate model for each converter configuration, i.e., isolated type [33] and based on CWVM incorporating parasitic elements is proposed. According to the inherent nonlinearity of the converter, the extended Kalman filter (EKF) is proposed to estimate the output voltage, magnetizing current of the transformer (which is a key parameter to diagnose core saturation of the transformer) and the inductor current. Moreover, by employing a sampled current compensation method, the estimation error is minimized. Employing an accurate model of different kind of energy converters is considered for different purposes such as loss minimization [34–36]. In [34], the efficiency comparison of the SiC-MOSFET and Si-IGBT-based converters is performed for DC-DC interleaved converters considering a range of switching frequency and output inductances. Reference [35] is focused on a switched-capacitor version of the DC-DC buck converter. Two techniques for reducing top-plate parasitic loss are proposed. In [36], the power losses components are minimized using the particle swarm optimization method (PSO) to optimize the Single Ended Primary Inductor Converter (SEPIC) used in LED lighting applications.

This paper is organized as follows. The accurate mathematical model of the converter is derived in Section 2. Using the derived model, the voltage observer based on EKF is designed in Section 3. Then in Section 4, sampled current compensation is proposed. Finally, the experimental and simulation results and their analysis for an experimental prototype of the converter are given in Section 5.

2. Mathematical Model of the Converter

In this section, the structure of the converter with the voltage observer is presented and the accurate model of the converter is derived.

2.1. Control Structure of the System

The structure of the converter (isolated type and based on CWVM) with the proposed voltage observer, is shown in Figure 2. The measured inductor current is used as the input of the observer.
Inaccuracy in the derived model of the converter can result in deviation of the observed voltage from the actual output voltage. Hence, for each configuration of the converter, the accurate model is derived, as follows.

The equivalent circuit of the isolated type configuration in which the elements on the secondary side of the transformer are referred to as the primary side is shown in Figure 3.

**Figure 2.** Control structure of two sensorless voltage controlled current-fed step-up converters. (a) Based on transformer (isolated type); (b) Based on Cockcroft-Walton Voltage Multiplier (CWVM).

### 2.2. The Precise Model of the Converter

Inaccuracy in the derived model of the converter can result in deviation of the observed voltage from the actual output voltage. Hence, for each configuration of the converter, the accurate model is derived, as follows.

The equivalent circuit of the isolated type configuration in which the elements on the secondary side of the transformer are referred to as the primary side is shown in Figure 3.
The considered parasitic elements are as follows:

- $R_{Ls}$: parasitic resistance of inductor $L_s$;
- $R_{L1}$: resistance of the transformer primary winding;
- $R_{DS}$: On-state resistance of the MOSFETs;
- $R'_{L2}$: secondary winding resistance of the transformer;
- $R'_D$: conduction resistance of the diodes;
- $V'_D$: forward voltage drop of diodes;
- $R'_{C0}$: equivalent series resistances (ESR) of capacitor $C_0$;
- $R'_{Cb}$: equivalent series resistances (ESR) of capacitor $C_b$.

The prime symbol (′) referred to as the primary side of the transformer.

The system state variables vector is:

$$x(t) = [i_{Ls}(t), v_{Cb'}(t), i_{Lm}(t), v_{Co'}(t)]^T$$

where $i_{Ls}(t)$, $v_{Cb'}(t)$, $i_{Lm}(t)$ and $v_{Co'}(t)$ are inductance $L_s$ current, capacitor $C_b'$ voltage, magnetizing inductance of the transformer $L_m$ current and capacitor $C_o'$ voltage, respectively.

In order to derive the dynamic model of the converter, several techniques can be applied. Here, the averaging technique is considered, in which the state space equations in different switching states are written and averaged in a switching cycle. The detailed description of the converter equations in different switching states are revealed in [31,37]. According to the state of the switches (ON/OFF), operation of the converter in a switching period ($T_s$) is divided into two states for a single-stage CW-VM. Using the conduction paths of the converter through capacitors and diodes in these switching states, the equations can be derived.

**State I:** $S_1$: ON & $S_2$: OFF, which lasts for $dT_s$ ($d$ is the duty cycle). The voltage across $L_1$ is equal to the input voltage ($V_{in}$); therefore, $i_{L1}$ increases, while $L_2$ transfers the stored energy to the CW-VM. $L_2$ is discharged with respect to the applied voltage.

**State II:** $S_1$: OFF & $S_2$: ON which lasts for $(1-d)T_s$. In this state, energy stored in $L_1$ during state I transfers to $L_2$ and the CWVM; therefore, $L_1$ and $C_o$ capacitors of CWVM are charged, and $C_1$ is discharged.
Using the relations governed the converter, state-space models in the two states can be described; state 1: $KX = A_1X + B_1U$, state 2: $KX = A_2X + B_2U$. Large signal-averaged equations $KX = AX + BU$ can be determined by the circuit averaging technique wherein $A = dA_1 + (1 - d)A_2$ and $B = dB_1 + (1 - d)B_2$.

The average state-space model of the system is obtained based on the assumption that the converter works in continuous conduction mode (CCM) as follows:

$$\dot{x}(t) = Ax(t) + Bu(t)$$

where

$$A_{11} = \left( R_{Ls} + R_{DS} + D'(R_{L1} + R'_{L2} + 2R'_{D} + R'_{Ch} + \frac{R_{Ch}'R_{Co}'}{R_{Ch} + R_{Co}}) \right) \frac{1}{L_s}$$

$$A_{13} = \frac{D'(R'_{L2} + 2R'_{D} + R'_{Ch} + \frac{R_{Ch}'R_{Co}'}{R_{Ch} + R_{Co}})}{L_s}$$

$$A_{14} = -\frac{D'(1 - \frac{R_{Co}'}{R_{Ch} + R_{Co}})}{L_s}$$

$$A_{31} = \frac{D'(R'_{L2} + 2R'_{D} + R'_{Ch} + \frac{R_{Ch}'R_{Co}'}{R_{Ch} + R_{Co}})}{L_{m1}}$$

$$A_{33} = -\frac{(R'_{L2} + 2R'_{D} + R'_{Ch} + \frac{R_{Ch}'R_{Co}'}{R_{Ch} + R_{Co}})}{L_{m1}}$$

$$A_{34} = \frac{(1 - 2D)(1 - \frac{R_{Co}'}{R_{Ch} + R_{Co}})}{L_{m1}}$$

$$D' = 1 - D$$

$D$, $V_{in}$ and $R_{o}'$ are duty ratio, input voltage, and the load resistance, respectively.

The equivalent circuit of the CWVM based converter is represented in Figure 4.

**Figure 4.** The accurate model of the step-up DC-DC converter based on CWVM.

The considered parasitic elements are as follows: $R_{L1}$ and $R_{L2}$ are the resistance of inductor $L_1$ and $L_2$, respectively. $R_{DS}$ and $R_D$ are the conduction resistance of the MOSFETs and diodes, respectively.
\( V_D \) is the forward voltage drop of diodes and \( R_{C1} \) and \( R_{C2} \) are the equivalent series resistance (ESR) of \( C_1 \) and \( C_2 \), respectively. The state variables of the converter are shown by Equation (11).

\[
x(t) = \begin{bmatrix} i_{L1}(t) \\ v_{C1}(t) \\ i_{L2}(t) \\ v_{C2}(t) \end{bmatrix}^T
\]

where \( i_{L1}(t) \), \( v_{C1}(t) \), \( i_{L2}(t) \) and \( v_{C2}(t) \) are \( L_1 \) current, \( C_1 \) voltage, \( L_2 \) current and \( C_2 \) voltage, respectively.

Assuming that the converter works in CCM, the average state-space model is obtained as follows:

\[
\begin{bmatrix}
\frac{di_{L1}(t)}{dt} \\
\frac{dv_{C1}(t)}{dt} \\
\frac{di_{L2}(t)}{dt} \\
\frac{dv_{C2}(t)}{dt}
\end{bmatrix} =
\begin{bmatrix}
A_{11} & A_{13} & A_{14} & \frac{i_{L1}(t)}{L_1} \\
A_{31} & -\frac{D'R}{L_2} & \frac{D'R}{L_2} & \frac{v_{C1}(t)}{L_2} \\
A_{13} & 0 & 0 & \frac{i_{L2}(t)}{L_2} \\
\frac{-D'R}{L_2} & \frac{D'R}{L_2} & -\frac{1}{L_2} & \frac{v_{C2}(t)}{L_2}
\end{bmatrix} +
\begin{bmatrix}
\frac{1}{L_1} \\
0 \\
0 \\
0
\end{bmatrix} \begin{bmatrix}
V_{in} \\
V_D
\end{bmatrix}
\]

where

\[
A_{11} = \frac{(R_{L1} + R_{DS} + D'(R_D + R_{C1} + \frac{R_{R_{C2}}}{R_o+R_{C2}}))}{L_1}
\]

\[
A_{13} = \frac{D'(R_D + R_{C1} + \frac{R_{R_{C2}}}{R_o+R_{C2}})}{L_1}
\]

\[
A_{14} = \frac{D'(1 - \frac{R_{R_{C2}}}{R_o+R_{C2}})}{L_1}
\]

\[
A_{31} = \frac{D'(R_D + R_{C1} + \frac{R_{R_{C2}}}{R_o+R_{C2}})}{L_2}
\]

\[
A_{33} = \frac{(R_{L2} + R_D + R_{C1} + D'(\frac{R_{R_{C2}}}{R_o+R_{C2}}))}{L_2}
\]

\[
A_{34} = \frac{D'(1 - \frac{R_{R_{C2}}}{R_o+R_{C2}})}{L_2}
\]

where \( R_o \) is the load resistance.

### 3. Proposed Voltage Estimation Strategy

According to the nonlinear behaviour of the foregoing DC-DC converter, EKF is used to estimate output voltage and noise filtering of the sampled inductor current. The EKF algorithm linearizes the nonlinear equations around the estimated values, and calculates the estimated values using the linearized equations.

In Section 2, the precise model of the converter has been derived. For the implementation of digital control, the first step is to transform the state equations to the discrete domain. The discrete state-space equation can be expressed as follows.

\[
X[k] = f(X[k-1], u[k], w[k-1])
\]

\[
X[k] = e^{AT}X[k-1] + A^{-1}(e^{AT} - I)Bu[k] + w[k-1]
\]

where \( u[k] \) is the system input at the \( k \)th sample time and \( T \) is the switching period. Since the forward Euler approximation method (\( e^{AT} >> I + AT \)) causes the instability of the converter discrete model, the backward Euler approximation method is used (\( e^{AT} >> (I - AT)^{-1} \)). The measurement function is:

\[
Z[k] = HX[k] + v[k]
\]
of the measurement and modelling into consideration, respectively. There is no coupling between these noises. $H$ denotes the observation matrix ($H = \begin{bmatrix} 1 & 0 & 0 & 0 \end{bmatrix}$).

The EKF algorithm consists of two phases: prediction and update. The states are predicted in the first phase by using a mathematical model of the system. Then, the predicted states are updated and refined by using actual states measurement in the second phase. According to Equation (19), the prediction stage of EKF is:

$$\tilde{X}[k] = (I - AT)^{-1}X[k-1] + A^{-1}(I - AT)^{-1} - I)Bu[k] + w[k-1]$$  \hspace{1cm} (21)

$$\tilde{P}[k] = A[k]P[k-1]A^T(k) + Q$$  \hspace{1cm} (22)

The state and covariance prediction relations are presented in (21) and (22) in which $Q$ denotes the covariance of the process noise $w[k]$. $\tilde{X}[k]$ and $X[k-1]$ are prior and post-state estimations, of the $k$th and $[k - 1]$th switching periods, respectively. Similarly, $\tilde{P}[k]$ and $P[k-1]$ are the prior and post estimation covariances of the $k$th and $[k - 1]$th switching periods. The Jacobian matrix of the process is $A[k]$, which is given by:

$$A[k] = \frac{\partial f(X[k-1])}{\partial X[k-1]} = (I - AT)^{-1}$$  \hspace{1cm} (23)

The updated measurement matrices of EKF are as follows:

$$G_k = \tilde{P}[k]H^T \hspace{1cm} \frac{HP[k]H^T + R}{\tilde{P}[k]H^T}$$  \hspace{1cm} (24)

$$\hat{X}[k] = \tilde{X}[k] + G_k(Z[k] - H\tilde{X}[k])$$  \hspace{1cm} (25)

$$P[k] = (I - G_kH)\tilde{P}[k]$$  \hspace{1cm} (26)

The state and covariance update equations are presented in Equations (24)–(26) in which $G_k$ and $R$ are the Kalman filter gain and covariance of the measurement noise $v[k]$, respectively. The output voltage can be estimated through the above process only by sampling $V_{\text{in}}$ (input voltage) and $i_{\text{Ls}}$ (or $i_{\text{L1}}$ in the second configuration of the converter). Furthermore, the sampled inductor current can be filtered.

In order to estimate the average value of the output voltage, the observer needs the average value of the inductor current. Since the beginning of each switching period is considered as the sampling point of the current, the sampled current should be compensated to the average value. Considering $i_s$ as the sampled current, then:

$$i_s = i_v$$  \hspace{1cm} (27)

where $i_v$ is the valley value of the inductor current. The average value of the inductor current ($i_{\text{av}}$) can be obtained as follows:

$$i_{\text{av}} = i_v + \frac{1}{2}i_{\text{pp}}$$  \hspace{1cm} (28)

where $i_{\text{pp}}$ is the inductor current ripple which is calculated from Equation (29) for both configurations:

$$i_{\text{pp}} = \frac{DV_{\text{in}}T}{L}$$  \hspace{1cm} (29)

Therefore, the compensation method can be given as:

$$i_{\text{comp}} = i_s + \frac{1}{2}i_{\text{pp}}$$  \hspace{1cm} (30)

where $i_{\text{pp}}$ is obtained through the observer.

In Figure 5, a block diagram of the EKF implementation is shown.
Experimental tests are performed to specify the precision of the proposed voltage observer. The specifications are presented in Tables 1 and 2.

(a) Command Signals

A/D Module

Current Sensor

Digital Signal Processor (DSP)

PWM Controller

(b) START

Initialization of the estimated state vector

\( k = k + 1 \)

Prediction of the state vector

Calculation of the observation matrix which is a Jacobian of the non-linear measurement

Acquisition of a new measurement vector

calculation of the updated state vector

OUTPUT

Figure 5. Block diagram of the Extended Kalman Filter (EKF). (a) EKF implementation; (b) using EKF for converter control system.
Here, it is worth mentioning the main disadvantages of this technique for voltage estimation:

- Requiring the accurate model of the converter. Some parameters that are not easy to be included in the model may affect the accuracy of the model: such as life derating of passive elements, frequency dependency of winding resistance, the nonlinearity of diode voltage drop and switch conduction resistance.
- Computational budgetary: In every computational step of the EKF algorithm, the non-linear equations of the system model are linearized. Compared to the conventional system (using a voltage sensor), this method requires computation and estimation of the voltage value. Therefore, a digital processor is necessary to be employed in this technique.

4. Simulation & Experimental Results

Simulation and experimental tests are carried out and the results are shown in this section to prove the performance of the presented observer. The simulations are conducted for both ideal and non-ideal converters. A laboratory prototype for the CWVM based converter is provided and experimental tests are performed to specify the precision of the proposed voltage observer. The specifications are presented in Tables 1 and 2.

Table 1. Specifications of the CWVM based DC-DC Converter (simulation and laboratory prototype).

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>( V_{in} ) (Input Voltage)</td>
<td>12 V</td>
</tr>
<tr>
<td>( V_{out} ) (Output Voltage)</td>
<td>50 V</td>
</tr>
<tr>
<td>( f_s ) (Switching Frequency)</td>
<td>50 kHz</td>
</tr>
<tr>
<td>( L_1 )</td>
<td>2 mH</td>
</tr>
<tr>
<td>( R_{L1} )</td>
<td>20 mΩ</td>
</tr>
<tr>
<td>( L_2 )</td>
<td>0.5 mH</td>
</tr>
<tr>
<td>( R_{L2} )</td>
<td>20 mΩ</td>
</tr>
<tr>
<td>( C_1 ) &amp; ( C_2 )</td>
<td>220 µF</td>
</tr>
<tr>
<td>( R_{C1} ) &amp; ( R_{C2} )</td>
<td>0.1 Ω</td>
</tr>
<tr>
<td>( R_{DS} )</td>
<td>0.1 Ω</td>
</tr>
<tr>
<td>( R_D )</td>
<td>1 mΩ</td>
</tr>
<tr>
<td>( V_D )</td>
<td>0.8 V</td>
</tr>
</tbody>
</table>

Table 2. Specifications of the isolated type DC-DC Converter.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>( V_{in} ) (Input Voltage)</td>
<td>12 V</td>
</tr>
<tr>
<td>( V_{out} ) (Output Voltage)</td>
<td>50 V</td>
</tr>
<tr>
<td>( f_s ) (Switching Frequency)</td>
<td>40 kHz</td>
</tr>
<tr>
<td>( L_s )</td>
<td>650 µH</td>
</tr>
<tr>
<td>( R_{Ls} )</td>
<td>20 mΩ</td>
</tr>
<tr>
<td>( L_{m} )</td>
<td>650 µH</td>
</tr>
<tr>
<td>( N_1 ) (Primary Winding Number of Turns)</td>
<td>31 turns</td>
</tr>
<tr>
<td>( N_2 ) (Secondary Winding Number of Turns)</td>
<td>13 turns</td>
</tr>
<tr>
<td>( R_{L1} )</td>
<td>20 mΩ</td>
</tr>
<tr>
<td>( R_{L2} )</td>
<td>20 mΩ</td>
</tr>
<tr>
<td>( C_b )</td>
<td>470 µF</td>
</tr>
<tr>
<td>( R_{CB} )</td>
<td>10 mΩ</td>
</tr>
<tr>
<td>( C_o )</td>
<td>990 µF</td>
</tr>
<tr>
<td>( R_{CO} )</td>
<td>10 mΩ</td>
</tr>
<tr>
<td>( R_{DS} )</td>
<td>0.1 Ω</td>
</tr>
<tr>
<td>( R_D )</td>
<td>1 mΩ</td>
</tr>
<tr>
<td>( V_D )</td>
<td>0.8 V</td>
</tr>
</tbody>
</table>
The simulation results are shown in Figures 6-9. In order to simulate the actual conditions, besides the non-idealities of the converter that is included in the simulations, measurement noise is also considered for the current sensor. The results for the start-up transient of the converter are shown here. The load resistance is assumed to be 270 Ω.

![Figure 6](image1.png)  
**Figure 6.** Simulation results for the ideal CWVM based DC-DC converter. (a) Sampled inductor current; (b) output voltage.

![Figure 7](image2.png)  
**Figure 7.** Simulation results for the non-ideal CWVM based converter. (a) Sampled inductor current; (b) output voltage.

Compensated sampled inductor current of \( L_1 \) (\( i_{L1} \)) for the simulated and accurate models of the converter are represented in Figure 6a or Figure 7a (Figure 6a for the ideal and Figure 7a for the non-ideal converter). The high-frequency variations in the measured \( i_{L1} \) are related to the simulated measurement noise. The proposed observer-based on EKF is used to estimate the output voltage, for which the results for actual and estimated values of \( V_o \) are shown in Figure 6b or Figure 7b (Figure 6b for the ideal and Figure 7b for the non-ideal converter). Considering these figures, two main results can be concluded:

- Good consistency in the transient response of the actual and estimated results validates the derived dynamic model. Both results show the same settling time of 22 ms. The steady-state error of the estimated output voltage is 3% (1.5 V) for the ideal converter and 0.2% (0.1 V) for the non-ideal converter. The accurate model (including parasitic elements) is used for both simulations; therefore, the model is not accurate in ideal simulation, and this is the main reason for the greater error in Figure 6b.
- Low variations of the estimated voltage (lower than 0.8 V) demonstrates that the proposed observer has filtered the measurement noise significantly.
Figure 8. Simulation results for the ideal isolated type DC-DC converter. (a) Sampled inductor current; (b) output voltage; (c) magnetizing inductor current.

Figure 9. Simulation results for the non-ideal isolated type DC-DC converter. (a) Sampled inductor current; (b) output voltage; (c) magnetizing inductor current.
The same simulation scenario as described for the CWVM based converter (for which the results are shown in Figures 6 and 7) is applied for a transformer-based converter, for which the output voltage is isolated. The results are shown in Figures 8 and 9.

The compensated sampled inductor currents for the simple and accurate model of the converter are shown in Figure 8a (for the ideal converter) and Figure 9a (for the non-ideal converter). The steady-state error of the estimated output voltage for the simple and accurate model of the converter are about 2.3% (1.1 V) and 1.4% (0.7 V) considering Figure 8b or Figure 9b. Although the amount of error is acceptable, it is greater than the estimated error for the CWVM based converter. The difference is mainly because of the core loss, which is not included in the model. Another issue which should be considered for transformer-based converter is measuring the magnetizing current of the transformer (magnetizing inductance is modelled as $L_2$). It must be estimated, as utilizing current sensor is not practical. The actual and estimated magnetizing inductor current for both simple and accurate models of the converter are also shown in Figure 8c (for the ideal converter) and Figure 9c (for the non-ideal converter). The steady-state errors of the estimated value of the magnetizing current are about 4.3% (40 mA) and 0.33 mA, respectively.

Simulation results for both configurations and the negligible errors between the actual and estimated values of the parameters demonstrate high accuracy (lower than 3% for output voltage and lower than 4.3% for magnetizing current in the worst conditions) and precision of the derived model of the converter and the observer.

A laboratory prototype of the CWVM based converter with the specifications described in Table 1 is provided. The experimental set up is shown in Figure 10.

![Experimental set-up of the current-fed CWVM based converter.](image)

The following steps are taken to evaluate the practical performance of the observer.

- The converter is used to feed a resistive load. The value of load resistance is 270 $\Omega$. The gating pulses are shown in Figure 11. In order to provide the required output voltage (50 V), the duty cycle is controlled to 45%. In addition, to provide safe commutation states, a sufficient overlap time (equal to 1 $\mu$s.) is considered for gating pulses. It is worth mentioning the state that both switches are off should be avoided [31]. The input voltage and CWVM voltage ($V_{CW}$) of the converter are shown in Figure 12.
- The current values of inductors are measured and a table of data for inductor currents are provided.
- Using the practical measurements and presented observer, the output voltage is estimated and compared to the experimental result.
The steady-state value of the inductor current and the output voltage are measured as 0.875 A and 50.1 V, respectively. Comparing the actual value of the output voltage (Figure 13b) and the estimated (0.53 V), which demonstrates the observer's precision and good performance. The output voltage (Figure 14) shows that the steady-state error of the estimated output voltage is 1.1%.

The converter is used to feed a resistive load. The value of load resistance is 270 Ω. Using the practical measurements and presented observer, the output voltage is estimated and provided.

The experimental waveforms for the current of inductor \( L_1 \) and output voltage are shown in Figure 13a,b, respectively. The steady-state value of the inductor current and the output voltage are measured as 0.875 A and 50.1 V, respectively.

The experimental waveforms for the non-ideal isolated type DC-DC converter are shown in Figure 9. The input voltage of the converter \( V_{in} \) (5 V/div); CWVM input voltage \( V_{CW} \) (20 V/div). (Duty cycle is 45% and the considered overlap is 1 μs).

The experimental waveforms for the applied gate-source voltage to the Metal Oxide Semiconductor Field Effect Transistors (MOSFETs) (5 V/div). (Duty cycle is 45% and the considered overlap is 1 μs).

The experimental waveforms for the CWVM based converter are shown in Figure 12. (a) The input voltage of the converter \( V_{in} \) (5 V/div); (b) CWVM input voltage \( V_{CW} \) (20 V/div).

The experimental waveforms for the CWVM based converter are shown in Figure 13. (a) Sampled inductor current (500 mA/div); (b) output voltage (20 V/div).
Using a digital oscilloscope, the current table of data is provided and used for estimating the output voltage. Comparing the actual value of the output voltage (Figure 13b) and the estimated output voltage (Figure 14) shows that the steady-state error of the estimated output voltage is 1.1% (0.53 V), which demonstrates the observer’s precision and good performance.

![Figure 14. The voltage observer result for output voltage estimation using the experimentally measured inductor current.](image)

5. Conclusions

In this paper, a voltage observer for two different configurations of a current-fed step-up DC-DC converter was proposed. The voltage estimation was implemented using an EKF voltage observer based on the accurate model of the system. In the worst conditions, the estimation error was 3%. In addition, it was shown by simulations that a more accurate model results in a more precise estimation. While the converter parasitic parameters were included, the estimation error was lower than 1%. Therefore, considering the parasitic elements in the model is necessary for a highly accurate estimation. Measuring noise also added to the current in the simulations and the effect of the noise on estimation accuracy is also considered. The simulation results showed that the presented observer can filter the noise effectively. The experimental results also indicated that by sampling the inductor current and applying the proposed voltage observer, the observed voltage converged to the actual value of the voltage, and the steady-state error of the output voltage was also minimized since parasitic parameters are considered and the sampled inductor current was compensated. Therefore, the proposed voltage observer can be used to eliminate the need for expensive high voltage sensors in high gain step-up DC-DC converters and also for measurement noise filtering. Two major limitations of using the observer are the accuracy of the dynamic model and required digital processor, and the computational burden.

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