Data from thermal testing of the Open Source Cryostage

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The data presented here is related to the research article “An open source cryostage and software analysis method for detection of antifreeze activity” (Buch and Ramløv, 2016) [1]. The design of the Open Source Cryostage (OSC) is tested in terms of thermal limits, thermal efficiency and electrical efficiency. This article furthermore includes an overview of the electrical circuitry and a flowchart of the software program controlling the temperature of the OSC. The thermal efficiency data is presented here as degrees per volt and maximum cooling capacity.

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**Keywords:**
Antifreeze proteins
Cryostage

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**Specifications Table**

<table>
<thead>
<tr>
<th>Subject area</th>
<th>Biology</th>
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<td>More specific subject area</td>
<td>Antifreeze protein research</td>
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<td>Type of data</td>
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<td>How data was acquired</td>
<td>Data was obtained on the OSC, which was developed as a cost efficient alternative to commercial temperature control units and cryostages.</td>
</tr>
<tr>
<td>Data format</td>
<td>Raw, analysed</td>
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E-mail address: jloerup@gmail.com (J.L. Buch).
### Experimental factors

Several supply voltages were tested to ensure maximum efficiency.

### Experimental features

The OSC was tested at increasing supply voltages. The maximum temperature differences over the Peltier Unit were recorded and plotted as absolute numbers, and as a function of voltage.

### Data source location

n/a

### Data accessibility

Data is within this article

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**Value of the data**

- The data may serve as a benchmark for further development of the design, or derivatives hereof.
- The data can easily be compared to competing designs, commercial or not.
- The information in this article may inspire others to develop their own designs, something the author highly recommends.

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1. **Data**

   Figs. 1 and 2 contain the thermal efficiency data. Figs. 3–5 contain information related to the experimental design.

2. **Experimental design, materials and methods**

   The OSC’s cooling capabilities were tested under room temperature conditions, with active air-cooling and a large heatsink. The Peltier unit used in the setup was rated for a max $\Delta T$ of 60 °C. The power supply was capable of delivering 5 A at varying voltages, all of which were tested. The Peltier module was controlled through PWM, so the duty cycles refer to power draw, relative to maximum.

   Several iterations of the OSC were tested before arriving at the design used in this article and the main research article [1]. The electrical design can be seen in Fig. 3.

   A custom PCB with the form factor of an Arduino shield was produced from the design seen in Fig. 4.

   The OSC has three main settings: LOAD, PID and FREEZE. LOAD will set the temperature of the stage to just below ambient, allowing loading without excessive condensation. FREEZE puts the Peltier unit at maximum power, which will freeze the sample within seconds. PID is the setting which is used during experimentation. A small thermistor provides the Arduino with constant temperature readings, and the Arduino in turn calculates Peltier power level from the temperature deviation from a given setpoint temperature. The setpoint temperature is related to the osmolality of the sample, as described in main article [1]. See Fig. 5 for flow chart.
Fig. 1. Cooling efficiency of OSC at increasing voltages.

Fig. 2. Cooling potential of cryostage at increasing voltages.
Fig. 3. Schematic overview of the Arduino controlled cryostage.

Fig. 4. PCB designed for the OSC.
Fig. 5. Flowchart showing the temperature control scheme in the OSC. The code is continuously executed from top to bottom.

Acknowledgments

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Transparency document. Supporting information

Transparency data associated with this article can be found in the online version at http://dx.doi.org/10.1016/j.dib.2016.06.056.
Appendix A. Supplementary material

Supplementary data associated with this article can be found in the online version at http://dx.doi.org/10.1016/j.dib.2016.06.056.

Reference